REMARKS

The Office Action of December 8, 2005 has been carefully considered. Reconsideration and allowance in view of the foregoing amendments and the following remarks is respectfully requested.

The drawing was objected to for various informalities. REPLACEMENT SHEETS 1 and 2 are submitted herewith.

Claims 1-6 were rejected as being unpatentable over Hauck in view of DeHon. The rejection states in part:

> Hauck et al did not teach that for at least one configured function, the second programmable connection circuit reversed the relative order of outputs of different basic blocks.... It would have been obvious...to have combined DeHon's system of reversal/rearrangement of data values with Hauck's system because of DeHon's teaching tha using reconfigurable logic for such functions provides performance, cost and flexibility advantages....

This rejection is respectfully traversed.

DeHon relates to interfacing with standard microprocessors using reconfigurable logic. The standard microprocessor has a control bus, an address bus and a data bus. No such processor is present in Hauck. There is no teaching or suggestion in the references themselves of combining the teachings of the references in the manner suggested.

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Withdrawal of the rejections and allowance of claims 1-6 is respectfully requested.

Respectfully submitted,

Michael J. Ure, Reg. 33,089

Dated: March 8, 2006

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